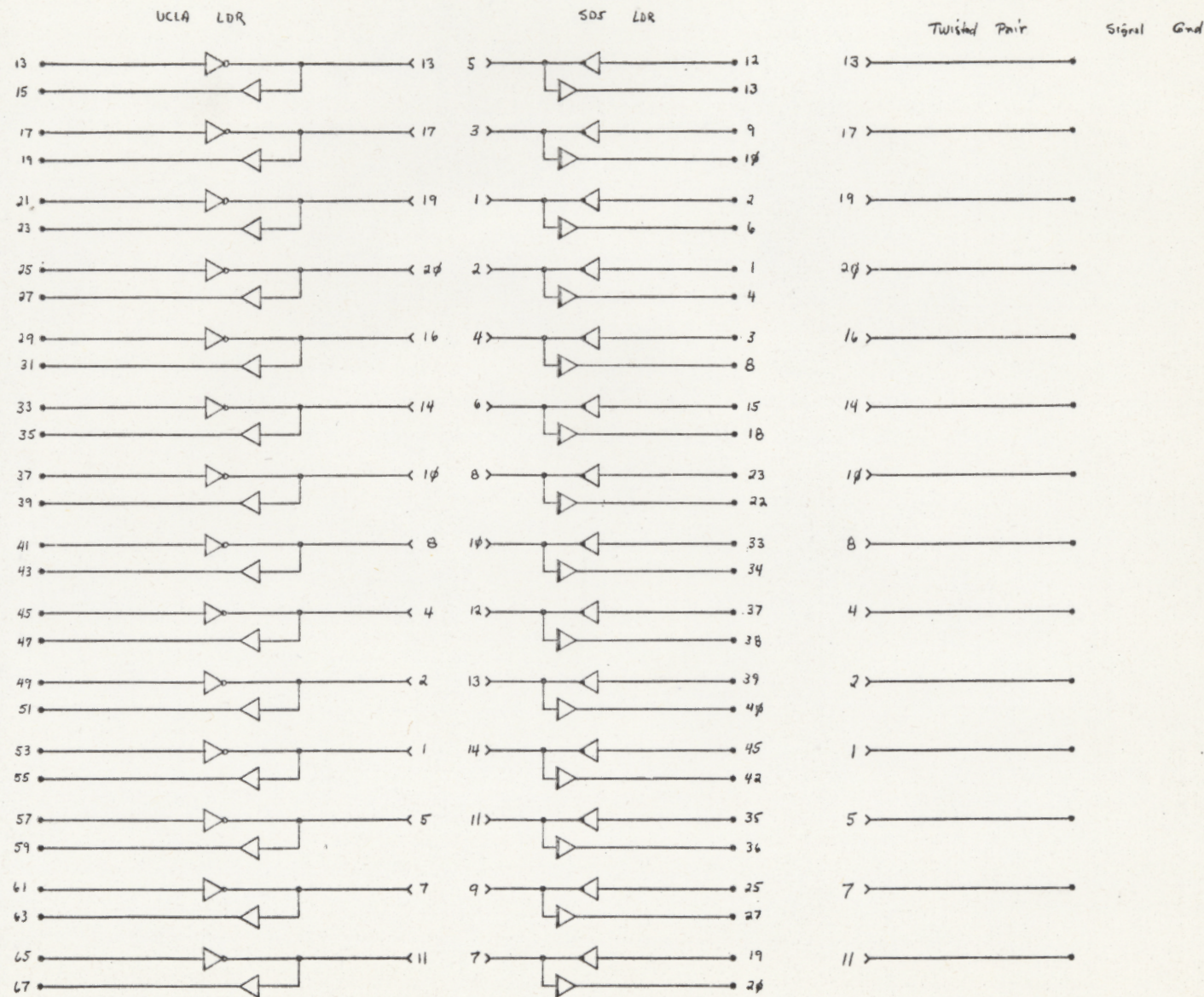


LINE DRIVER-RECEIVERS

M. Wingfield BH2917-A-9

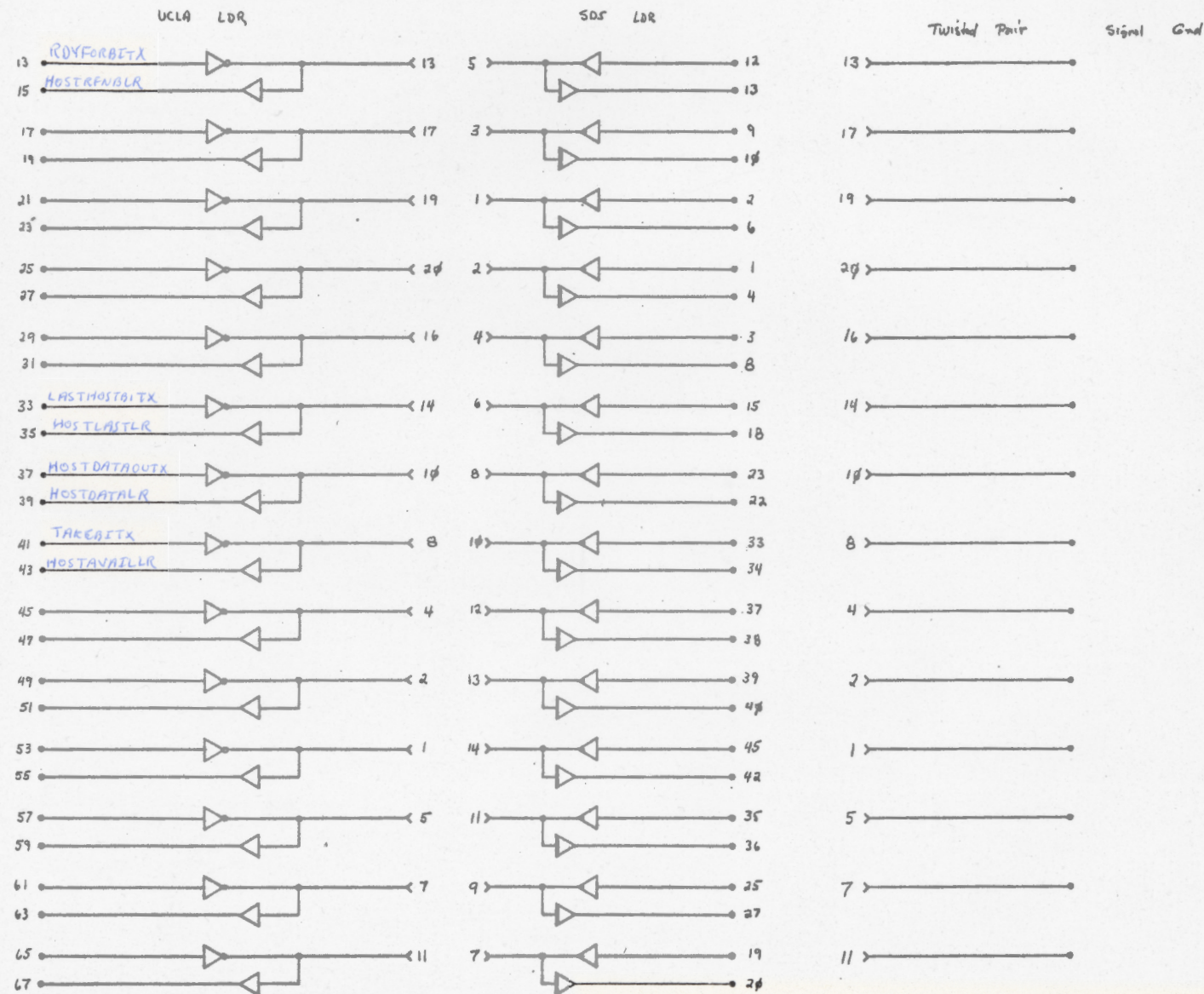


NOTE: Receivers invert for PNP logic inputs

AE801

LINE DRIVER-RECEIVERS

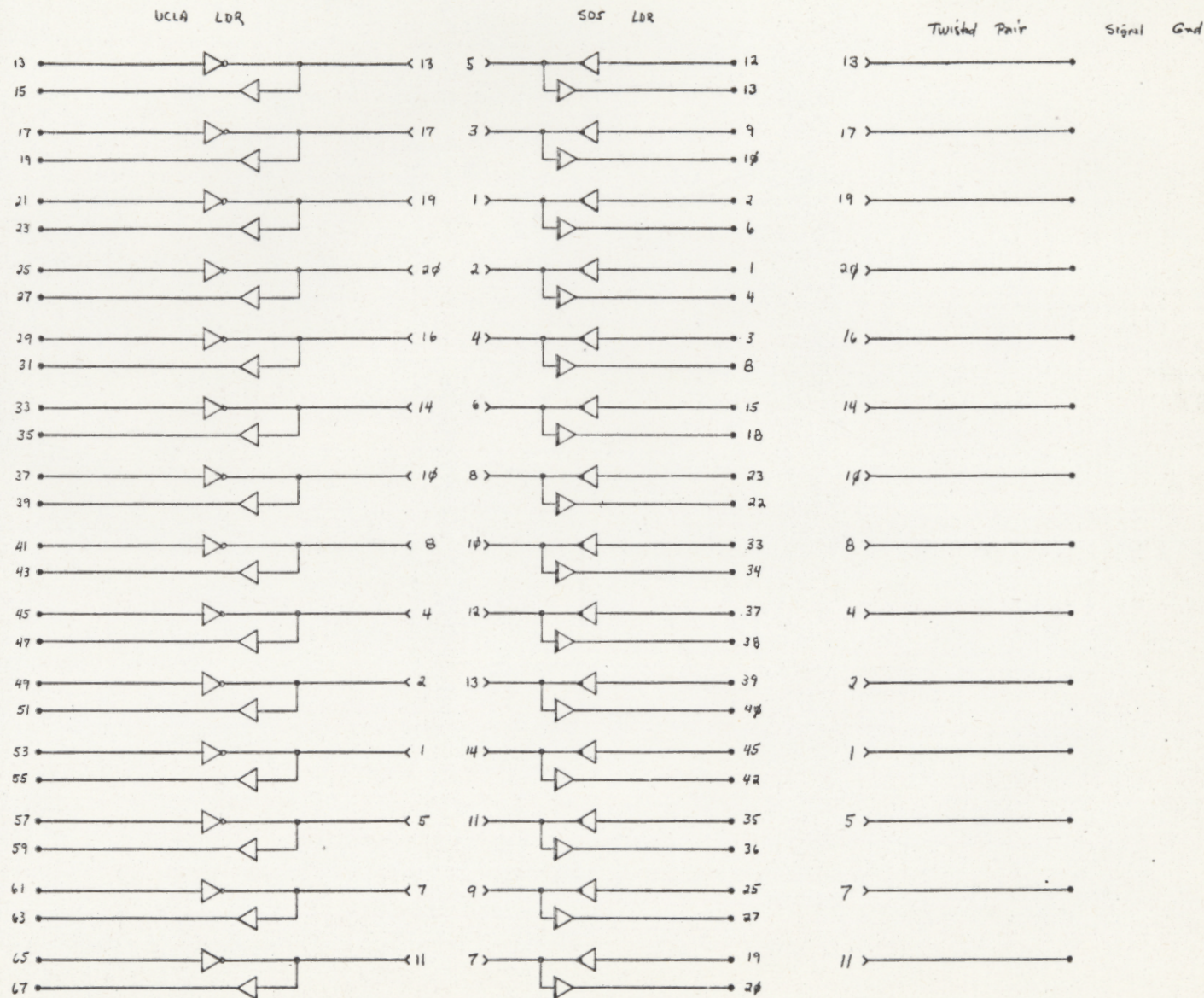
M. Wingfield BH0017-A-9



NOTE: Receivers invert
for PNP logic
inputs

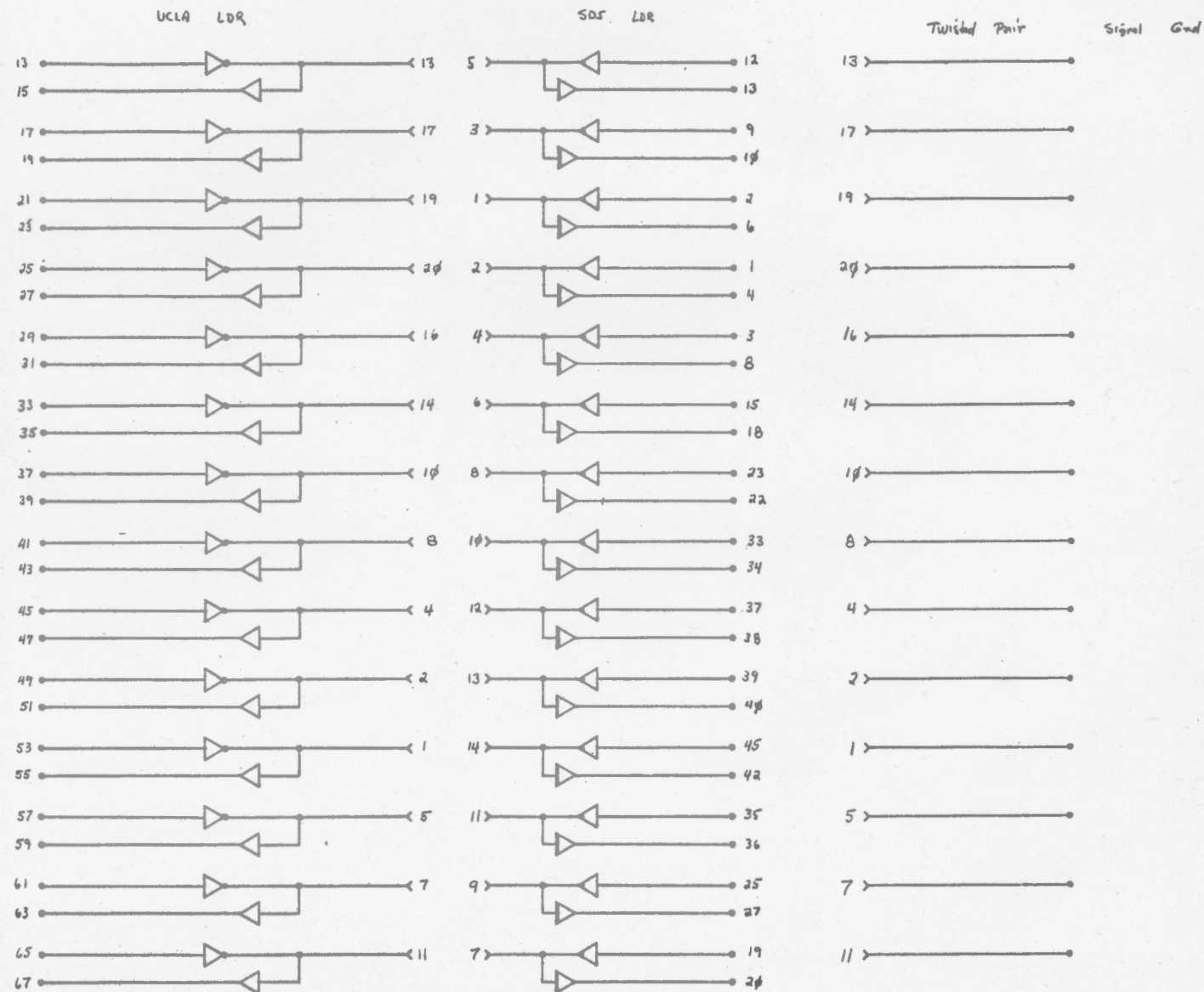
LINE DRIVER-RECEIVERS

M. Wingfield BH 2017-A-9



NOTE: Receivers invert
for PNP logic
inputs

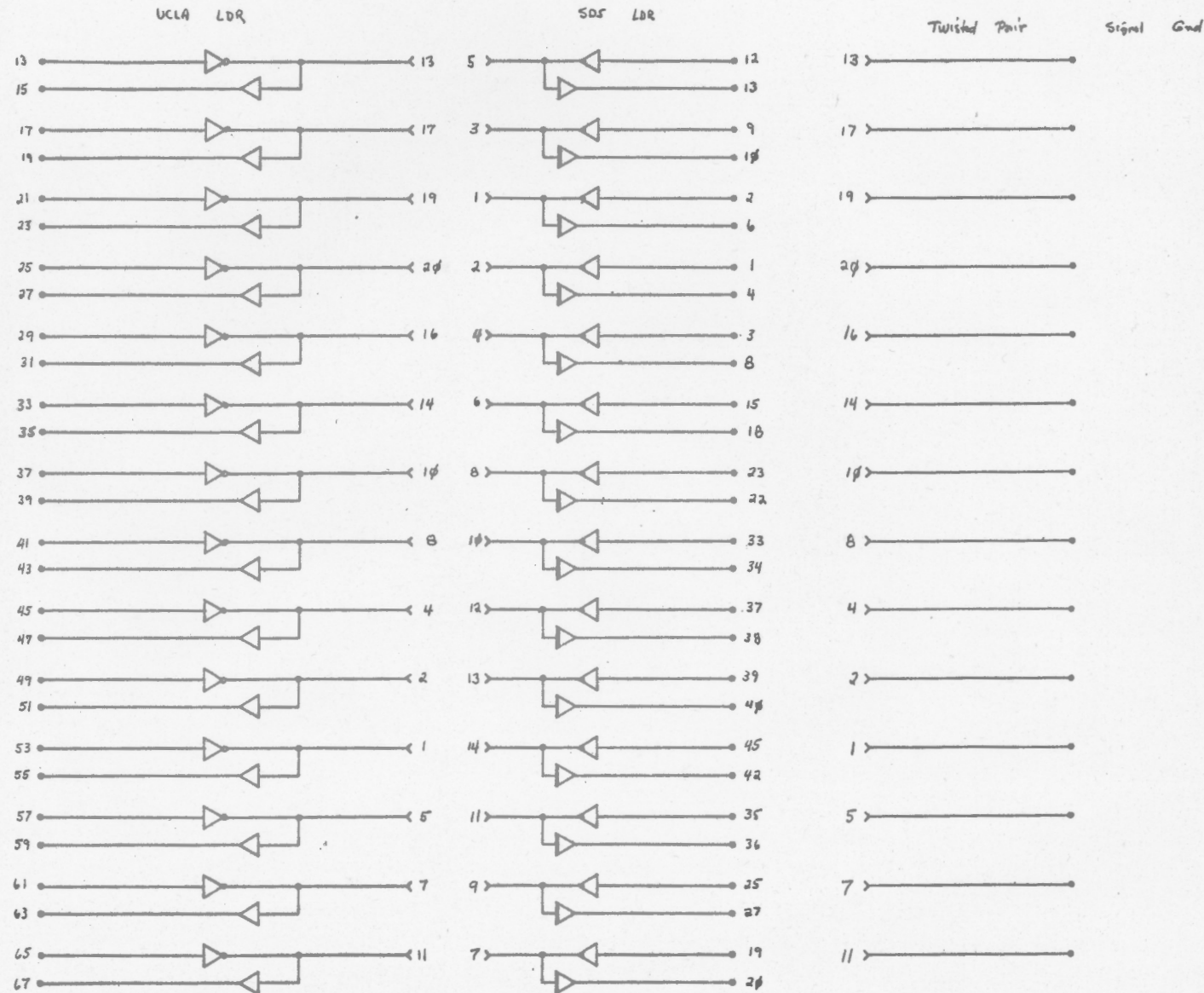
LINE DRIVER-RECEIVERS
M. Wingfield 3H2817-A-9



NOTE: Receivers invert
for PNP logic
inputs

AE804

LINE DRIVER-RECEIVERS
M. Wingfield BH 0017-A-9



NOTE: Receivers invert
for PNP logic
inputs

AE605

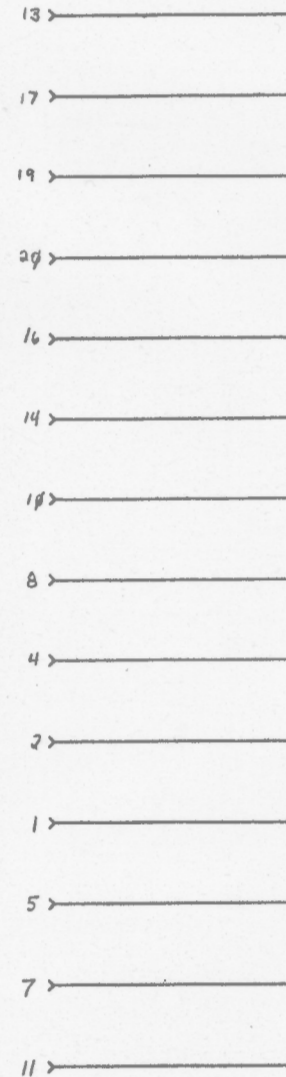
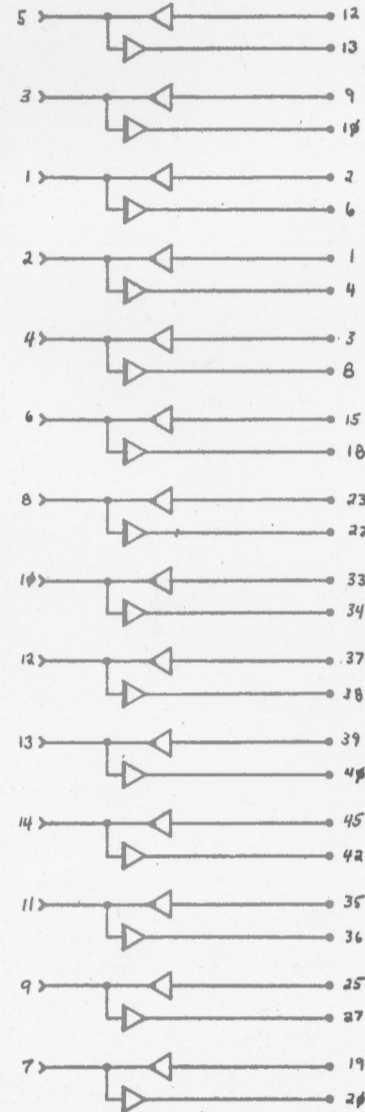
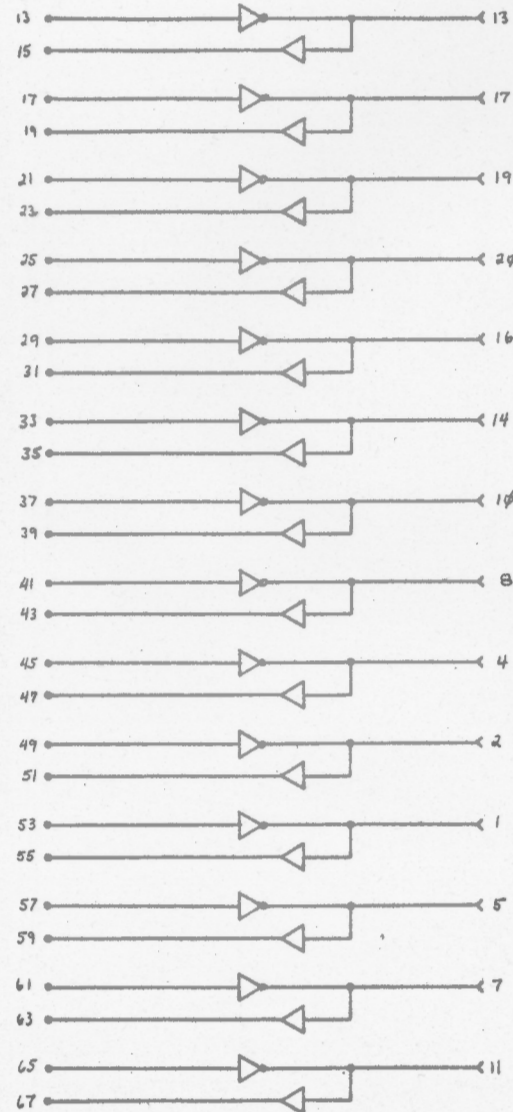
LINE DRIVER-RECEIVERS

M. Wingfield BH 2017-A-9

UCLA LDR

SDS LDR

Twisted Pair Signal Gnd

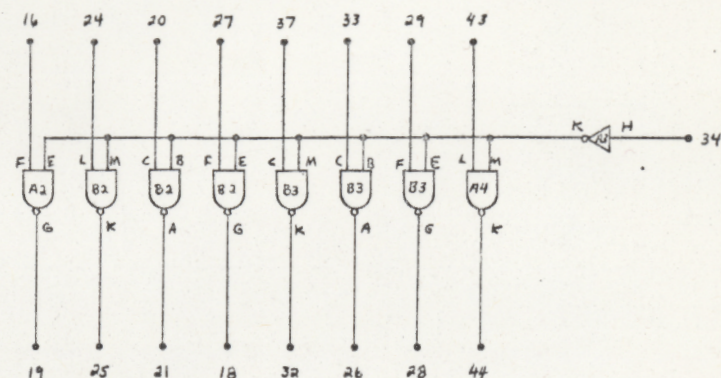
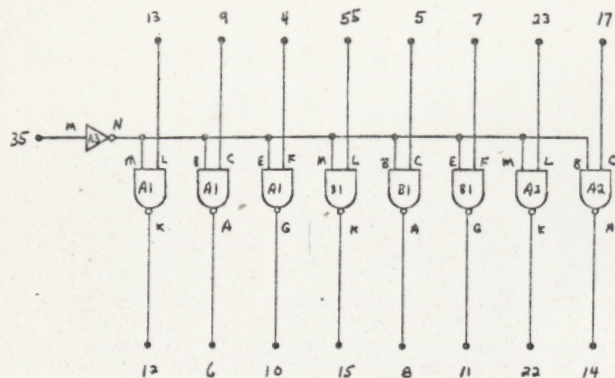


NOTE: Receivers invert for PNP logic inputs

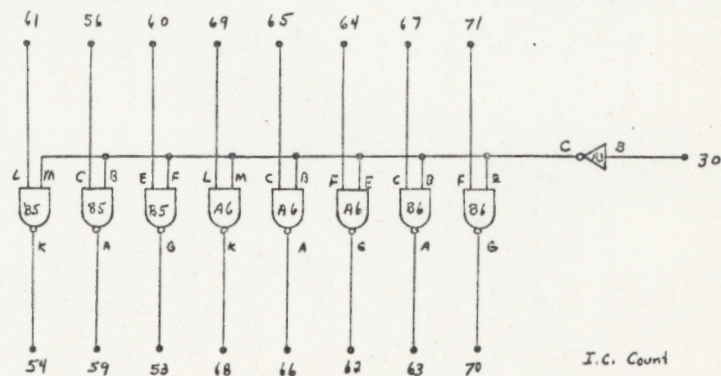
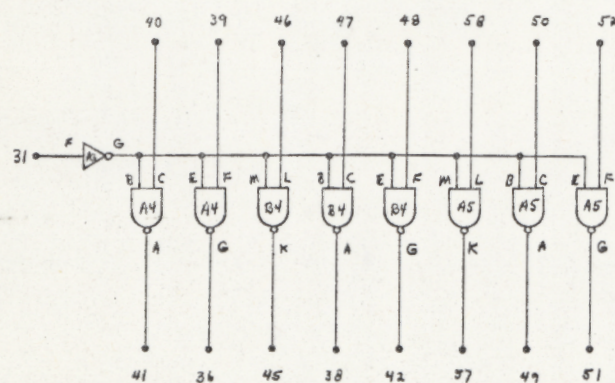
4-8 Bit Data Center

0004

M. Wingfield
8/28/77-A-9



6	5	4	3	2	1
SG162	SG162	SG162	SG162	SG162	SG162
SG162	SG162	SG162	SG162	SG162	SG162
A	B	C	D		



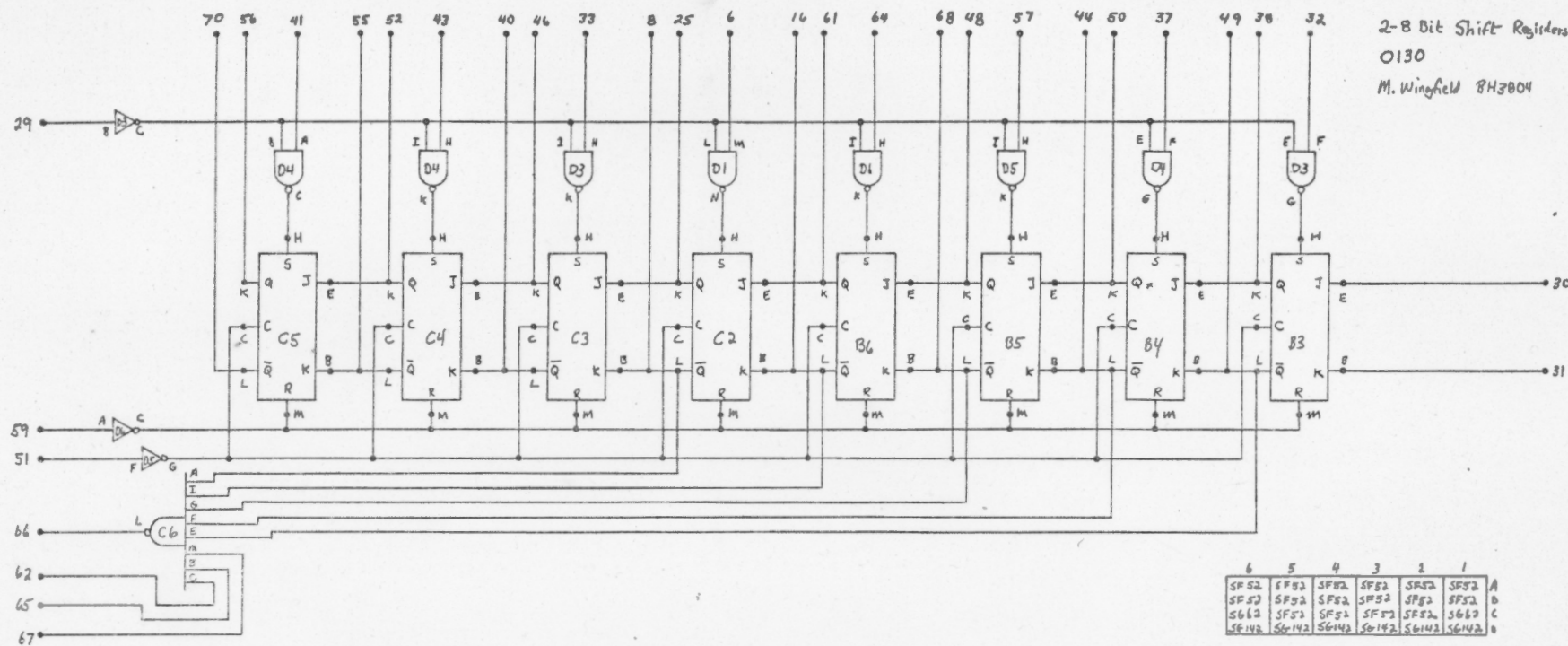
I.C. Count

#	Description	IC
1	SG-142	1
2	SG-162	11

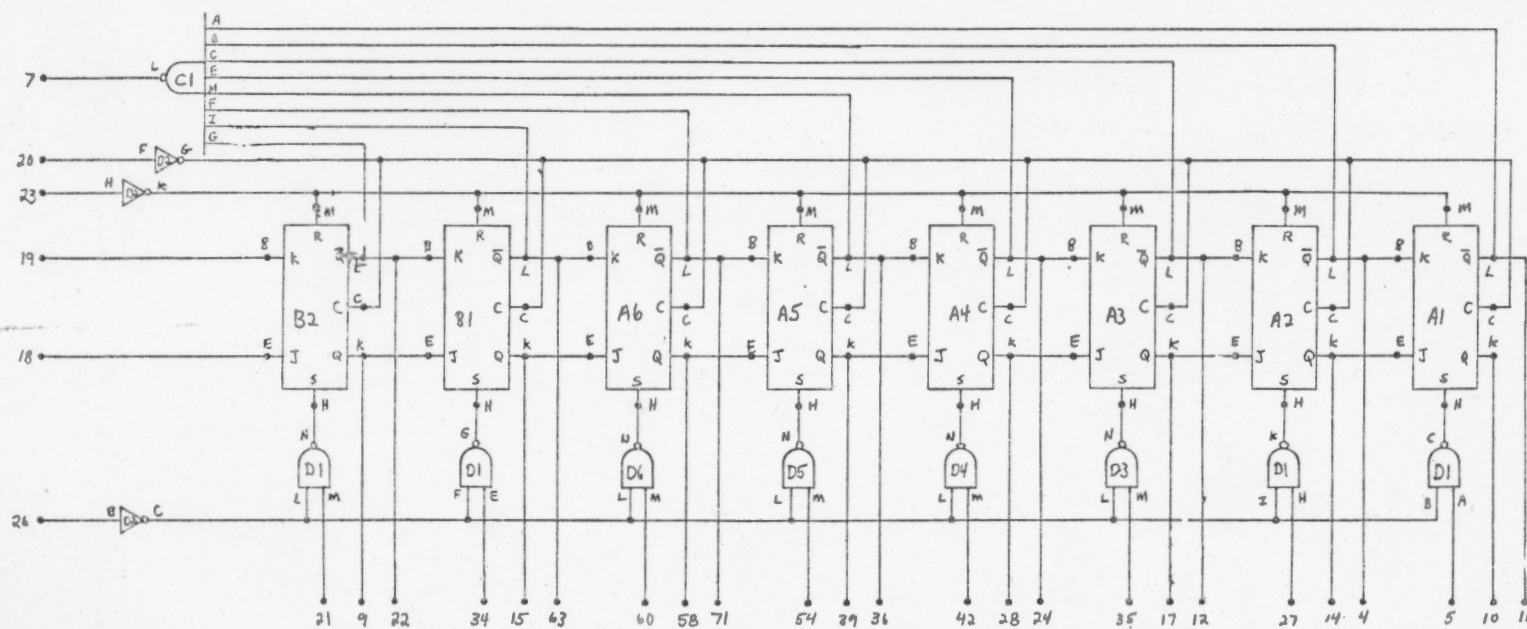
I/O: 6B

AE607

2-Bit Shift Registers
0130
M. Wingfield 8H3804



6	5	4	3	2	1
SF52	SF52	SF52	SF52	SF52	SF52
SF52	SF52	SF52	SF52	SF52	SF52
SF52	SF52	SF52	SF52	SF52	SF52
SF52	SF52	SF52	SF52	SF52	SF52
SF52	SF52	SF52	SF52	SF52	SF52
SF52	SF52	SF52	SF52	SF52	SF52
SF52	SF52	SF52	SF52	SF52	SF52



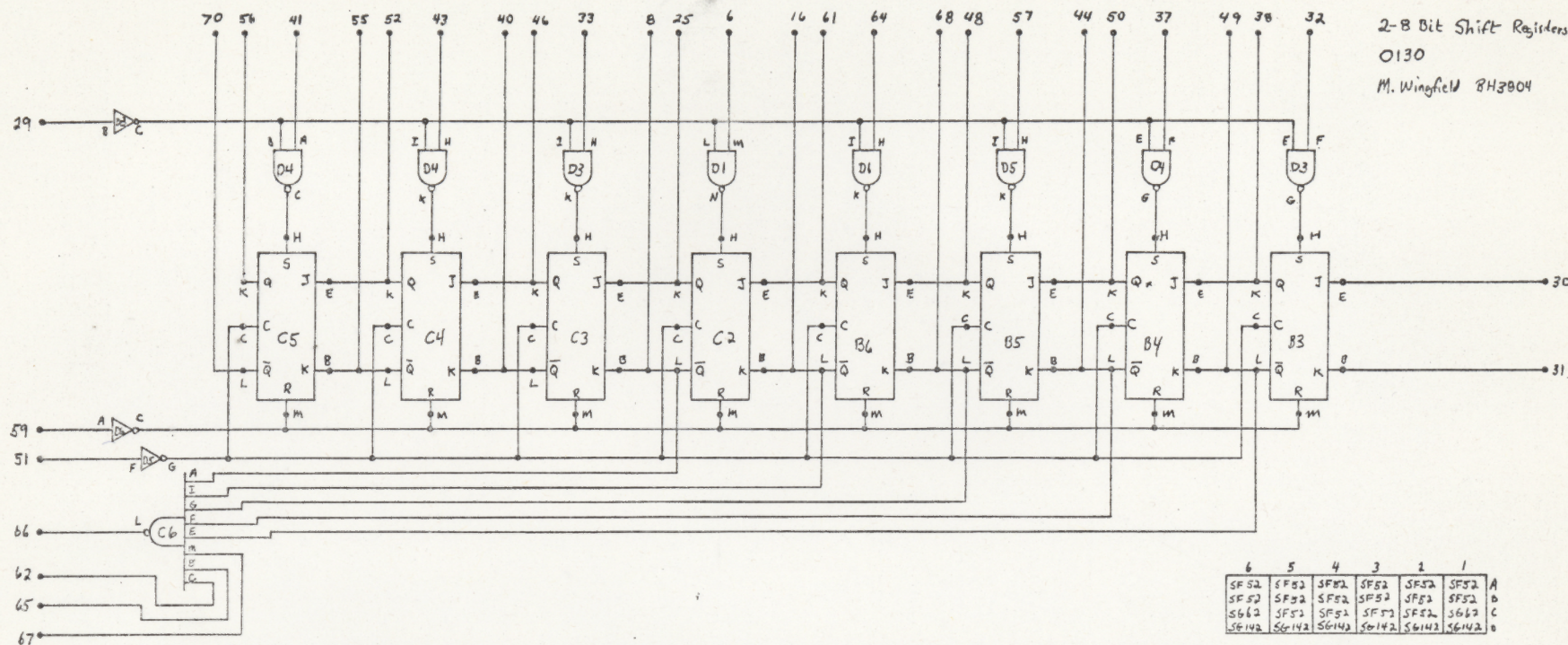
I.C. Count

#	Description	Totals I.C.
1	SF-52	16
2	SG-142	6
3	SG-62	2
		<hr/> 24 I.C.

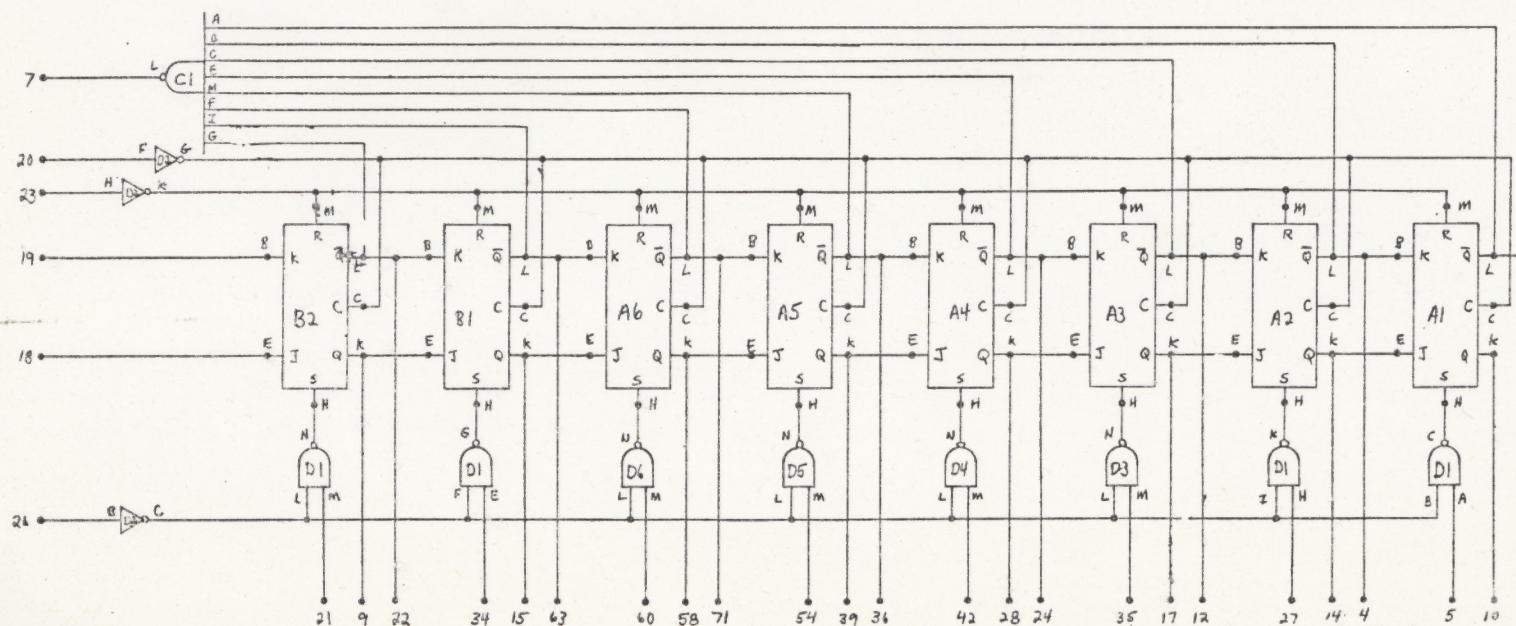
I/O: 63

AE408

2-8 Bit Shift Registers
0130
M. Wingfield 8H3B04



6	5	4	3	2	1	
SF52	SF53	SF52	SF52	SF52	SF52	A
SF52	SF53	SF52	SF52	SF52	SF52	B
S612	SF53	SF52	SF52	SF52	S612	C
SF142	S6142	S6142	S6142	S6142	S6142	D



I.C. Count

#	Description	Totals I.C.
1	SF-52	16
2	S6-142	6
3	S6-62	2

24 I.C.

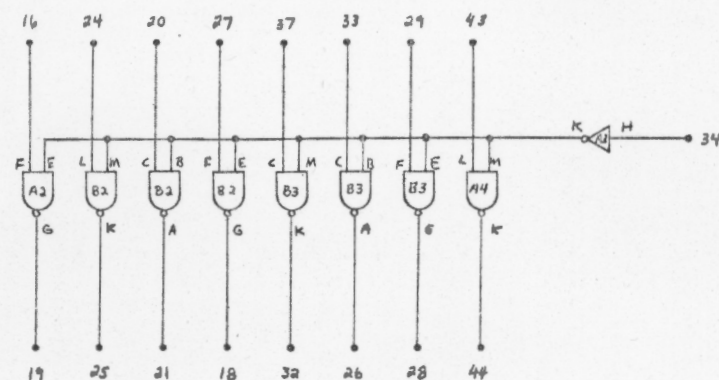
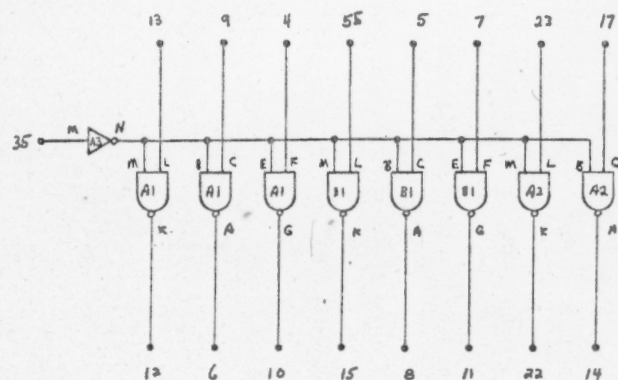
I/O: 63

AE809

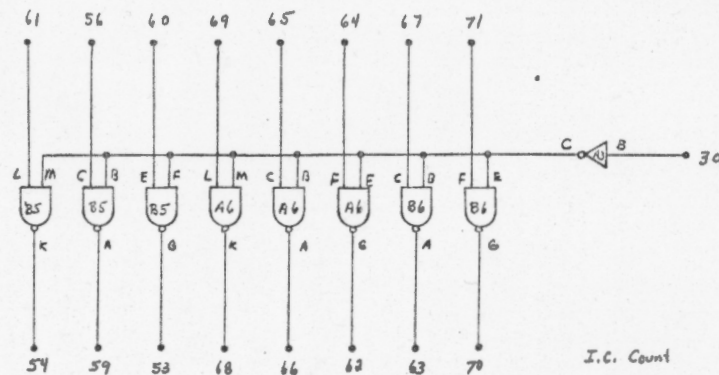
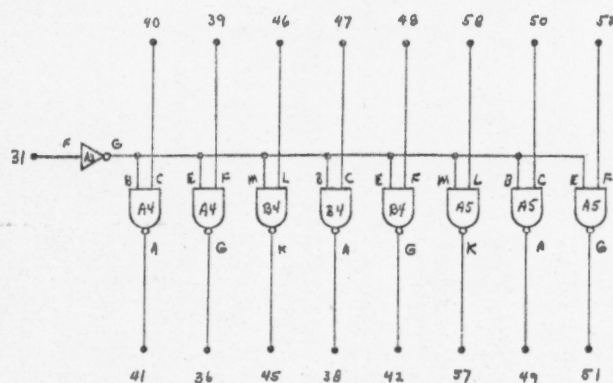
4 - 8 Bit Data Gates

0004

M. Wingfield
24/01/74



6	5	4	3	2	1	
SG162	SG162	SG162	SG162	SG162	SG162	A
SG162	SG162	SG162	SG162	SG162	SG162	B
						C
						D



I.C. Count

#	Description	I.C.
1	SG-162	1
2	SG-162	11

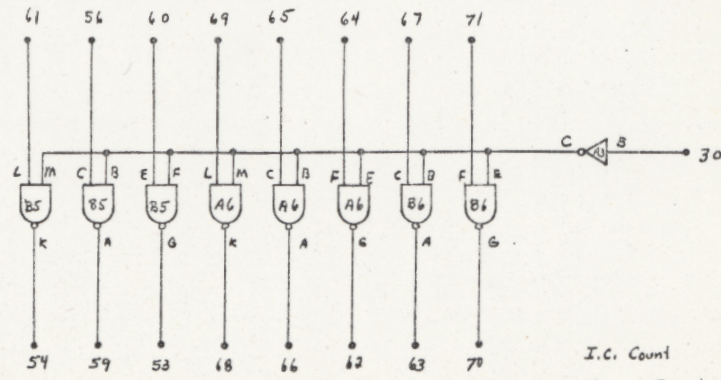
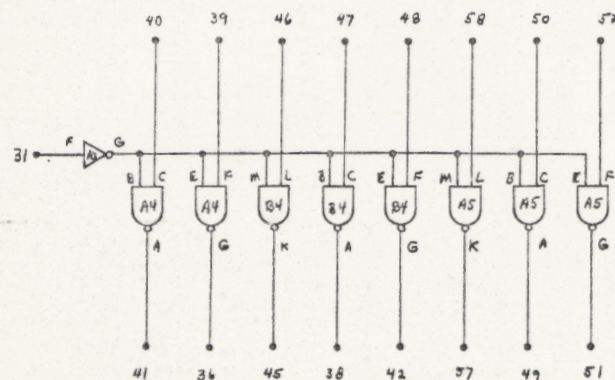
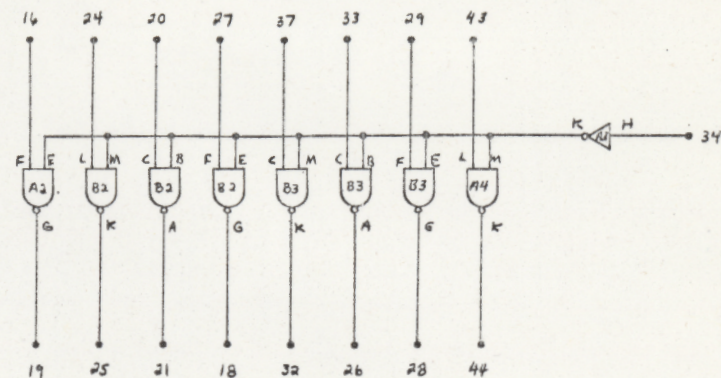
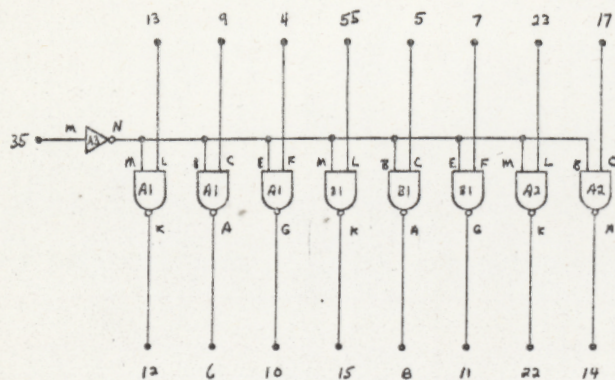
I/O: B

AE810

4-8 Bit Data Gates

0004

M. Wing Field
2428/7-A-7



6	5	4	3	2	1	
SG162	SG162	SG162	SG162	SG162	SG162	A
SG162	SG162	SG162	SG162	SG162	SG162	D
						C

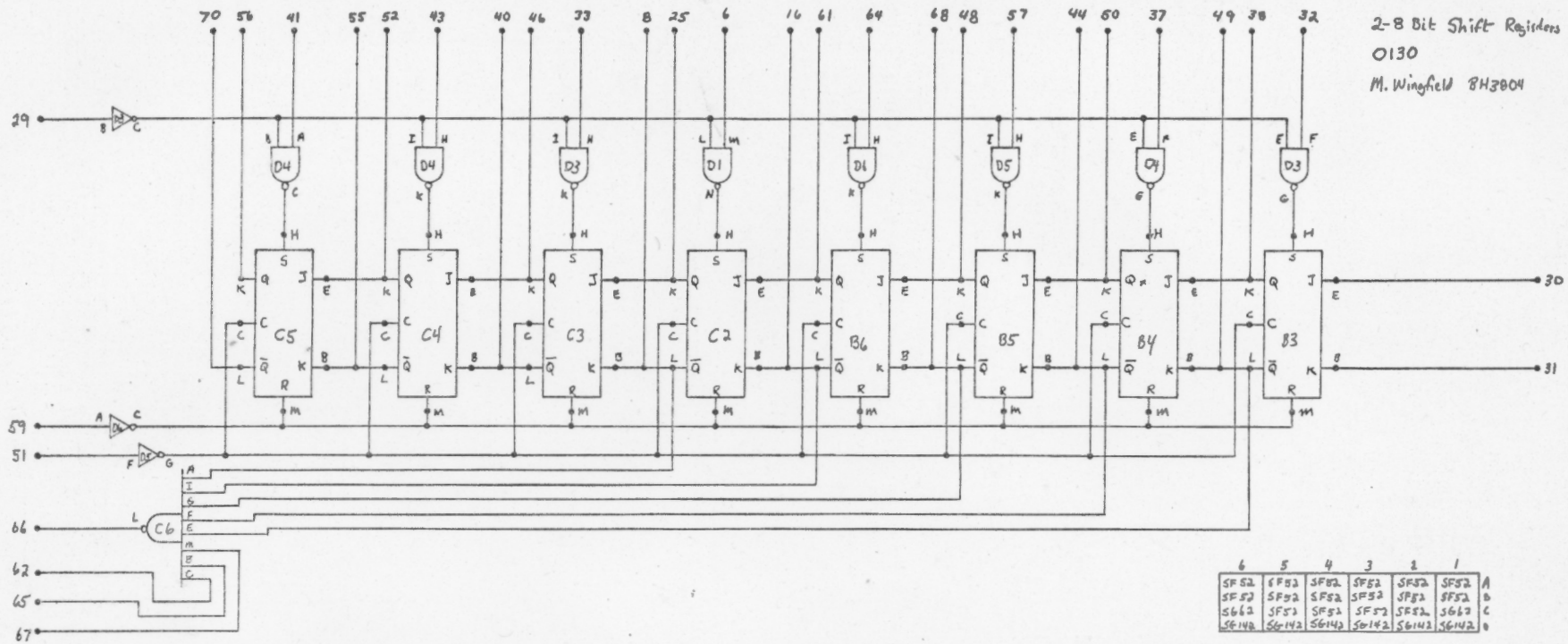
I.C. Count

#	Description	I.C.
1	SG-142	1
2	SG-162	11

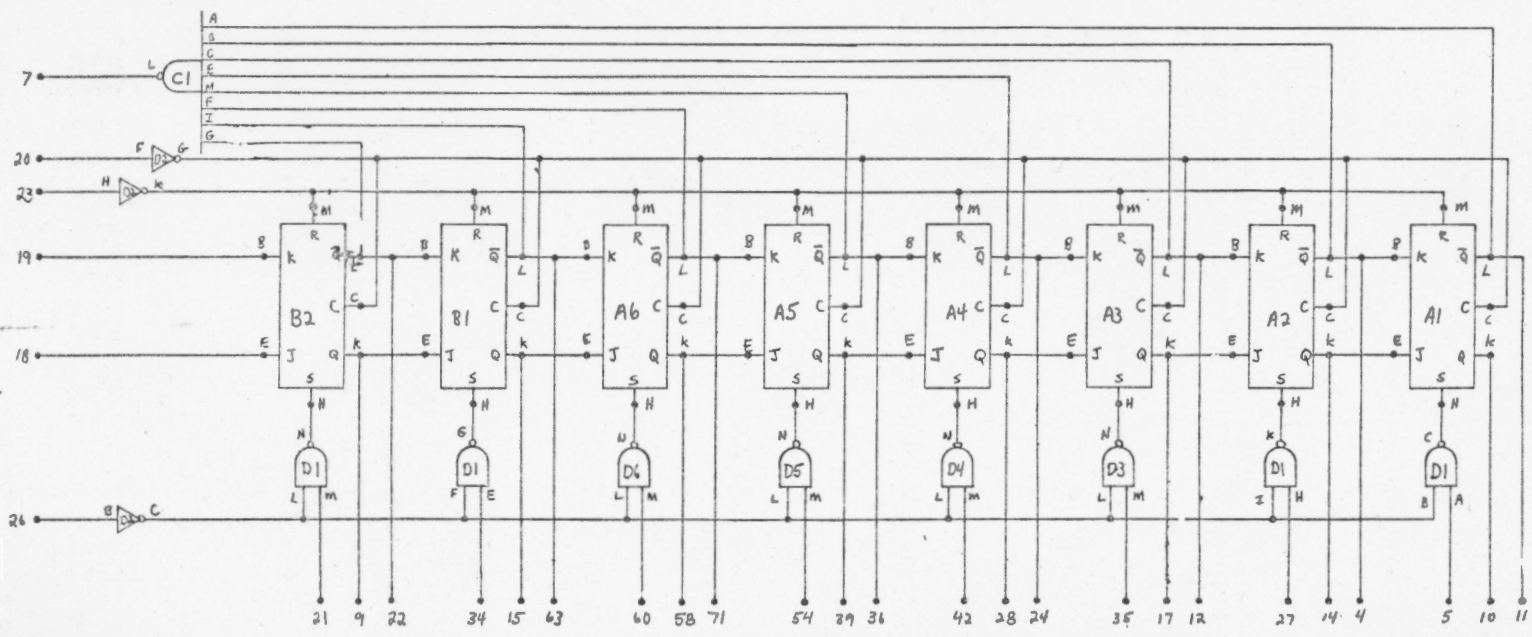
I/O: 68

AE811

2-8 Bit Shift Registers
 O130
 M. Wingfield BH3004



6	5	4	3	2	1	
SF52	SF52	SF52	SF52	SF52	SF52	A
SF52	SF52	SF52	SF52	SF52	SF52	B
S662	SF52	SF52	SF52	SF52	S662	C
S6142	S6142	S6142	S6142	S6142	S6142	D



I.C. Count

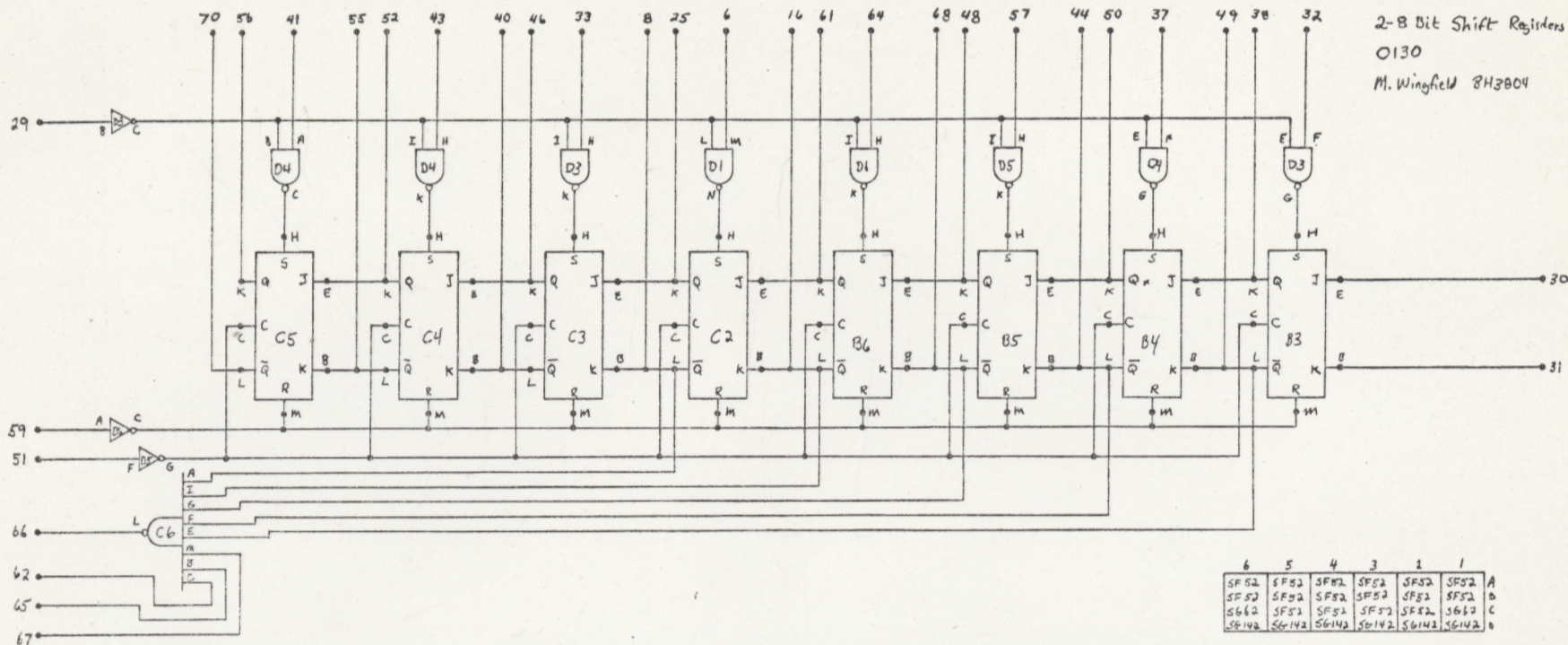
#	Description	Totals I.C.
1	SF-52	16
2	S6-142	6
3	S6-62	2
		<hr/> 24 I.C.

I/O: 63

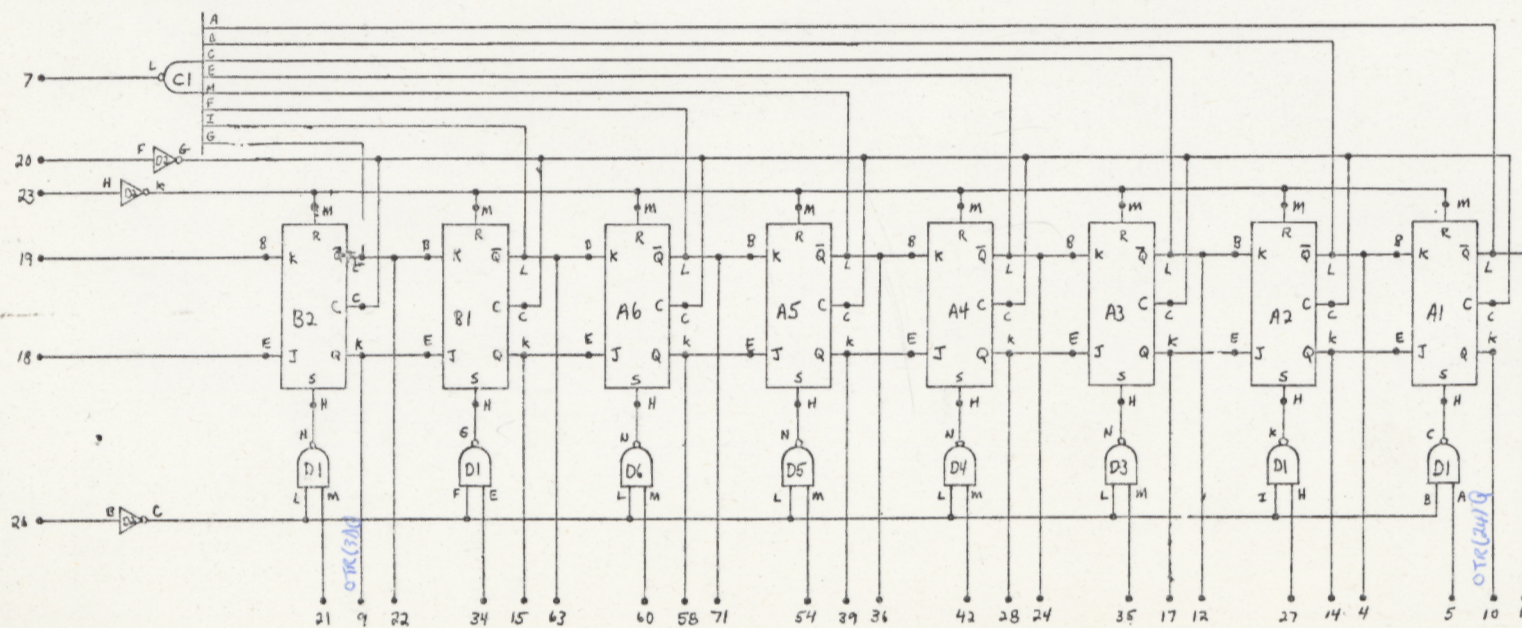
OTK0-15

AE812

2-B Bit Shift Registers
0130
M. Wingfield 8H2804



6	5	4	3	1	1
SF52	SF52	SF52	SF52	SF52	A
SF52	SF52	SF52	SF52	SF52	B
S662	SF52	SF52	SF52	S662	C
S6142	S6142	S6142	S6142	S6142	D



I.C. Count

#	Description	Tot/I.C.
1	SF-52	16
2	SG-142	6
3	SG-62	2
		<hr/> 24 I.C.

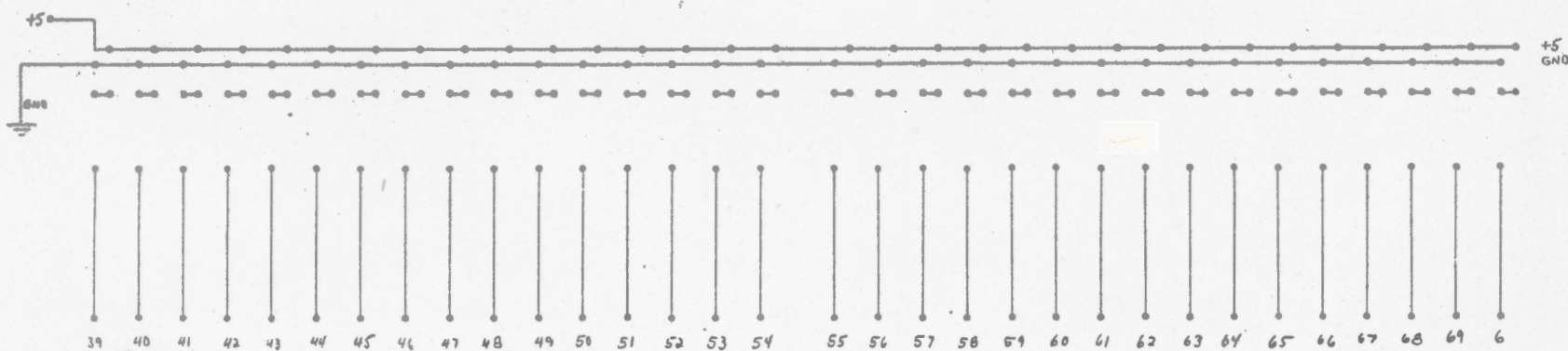
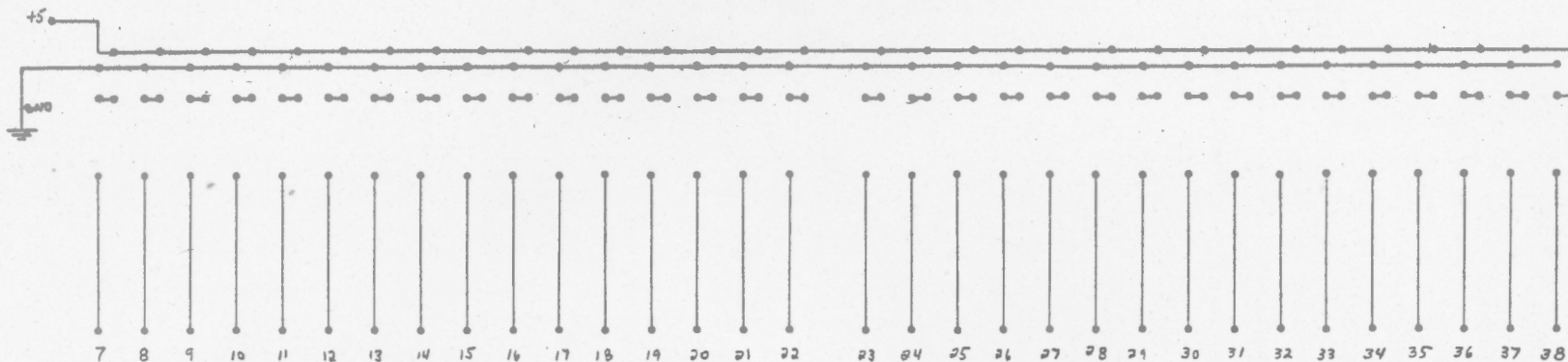
I/O: 63

OTR 16-31

AE013

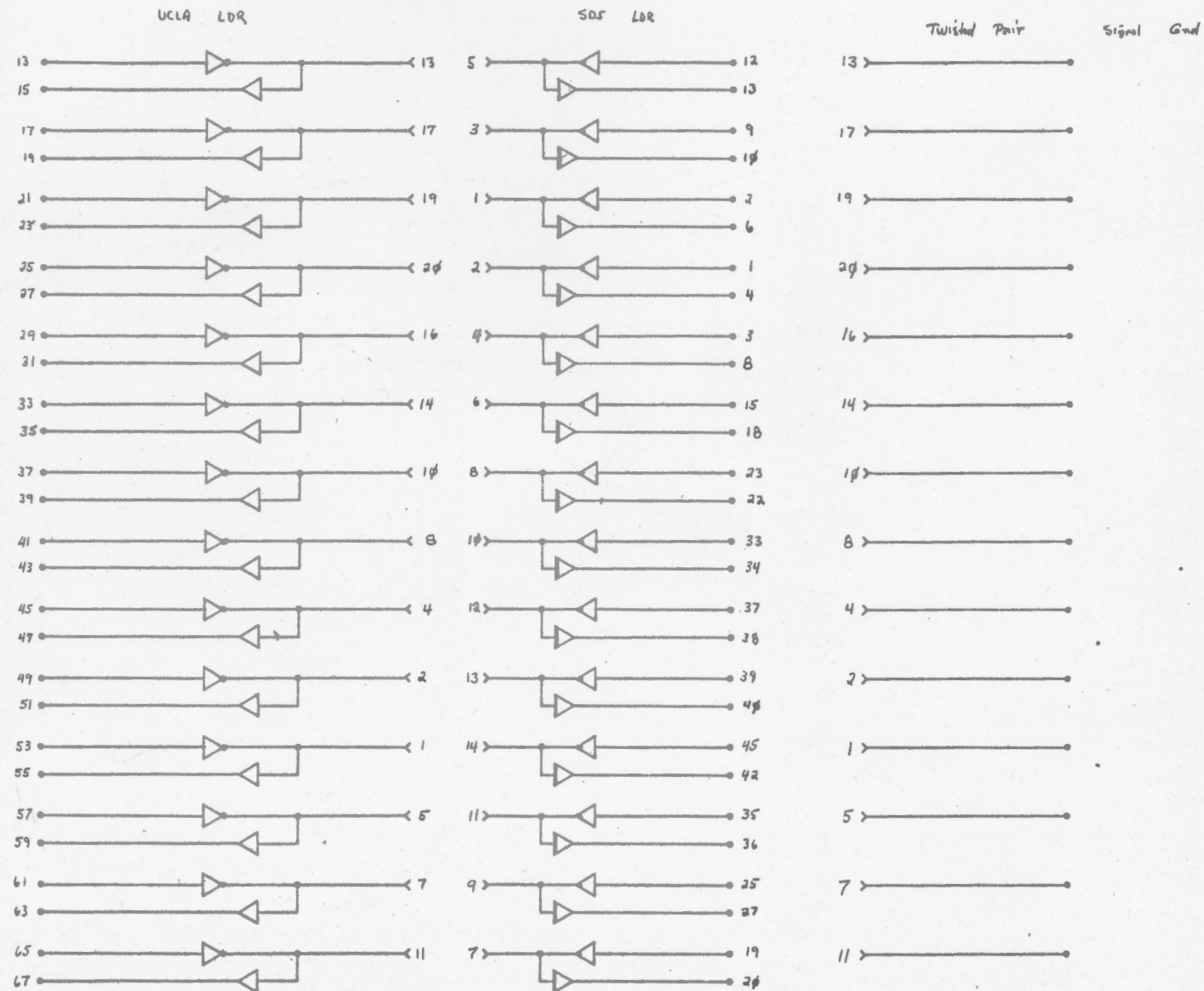
Discrete Component Board

M. Wingfield BH2B17-A-7



AE814

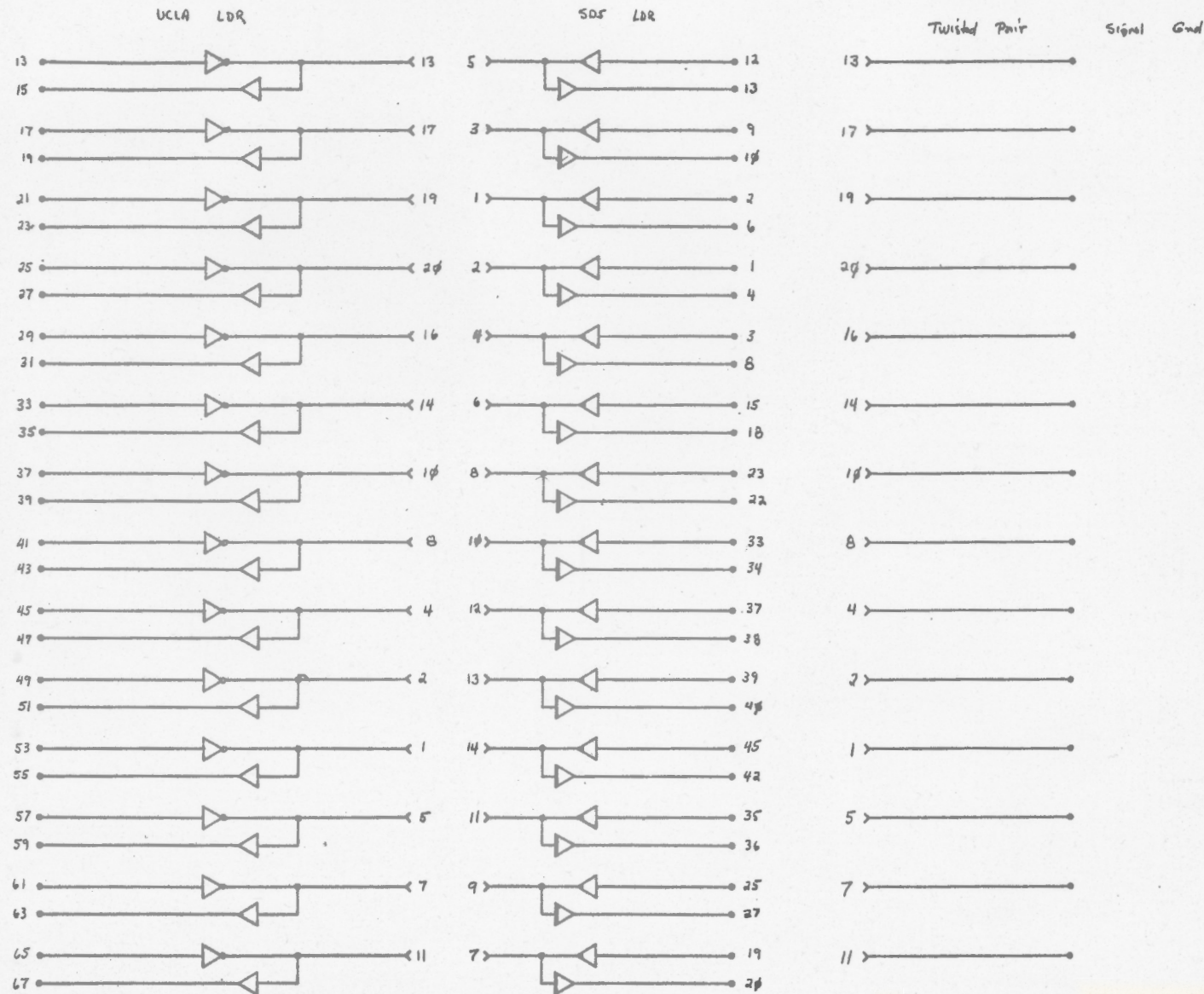
LINE DRIVER-RECEIVERS
M. Wingfield BH0017-A-9



NOTE: Receivers invert
for PNP logic
inputs

LINE DRIVER-RECEIVERS

M. Wingfield BH2017-A-9

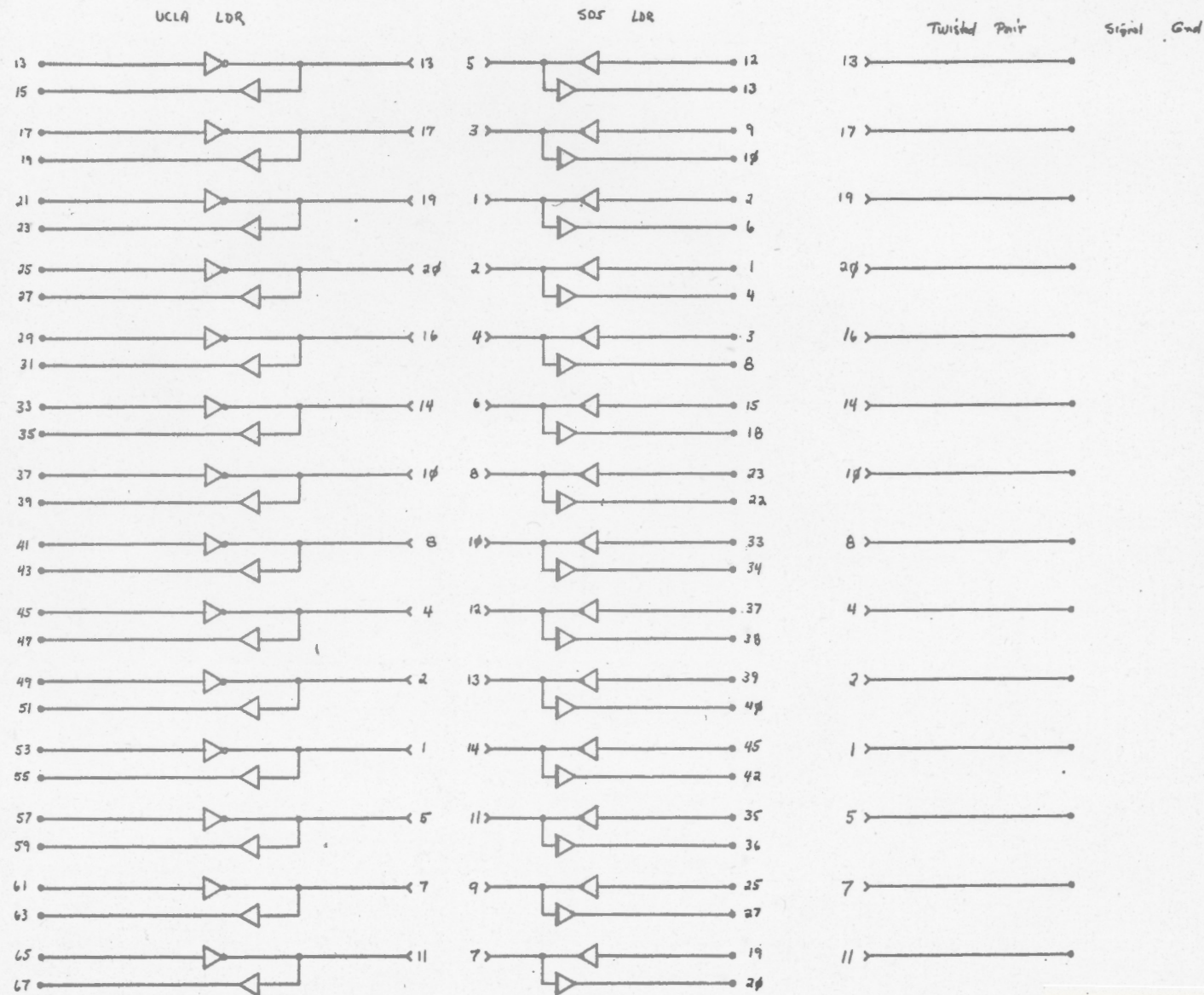


NOTE: Receivers invert for PNP logic inputs

AE616

LINE DRIVER-RECEIVERS

M. Wingfield 3H2017-A-9



NOTE: Receivers invert
for PNP logic
inputs

AE817

LINE DRIVER-RECEIVERS

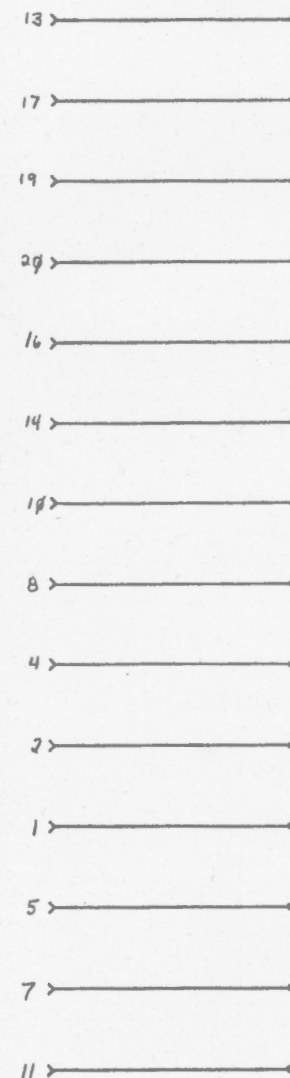
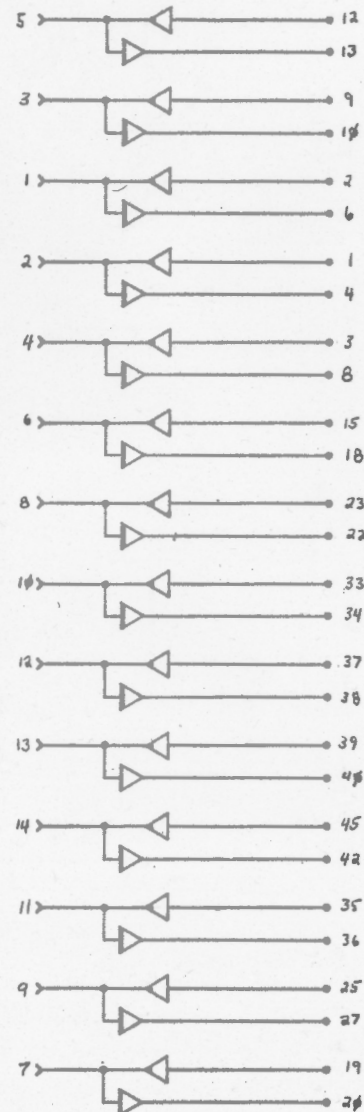
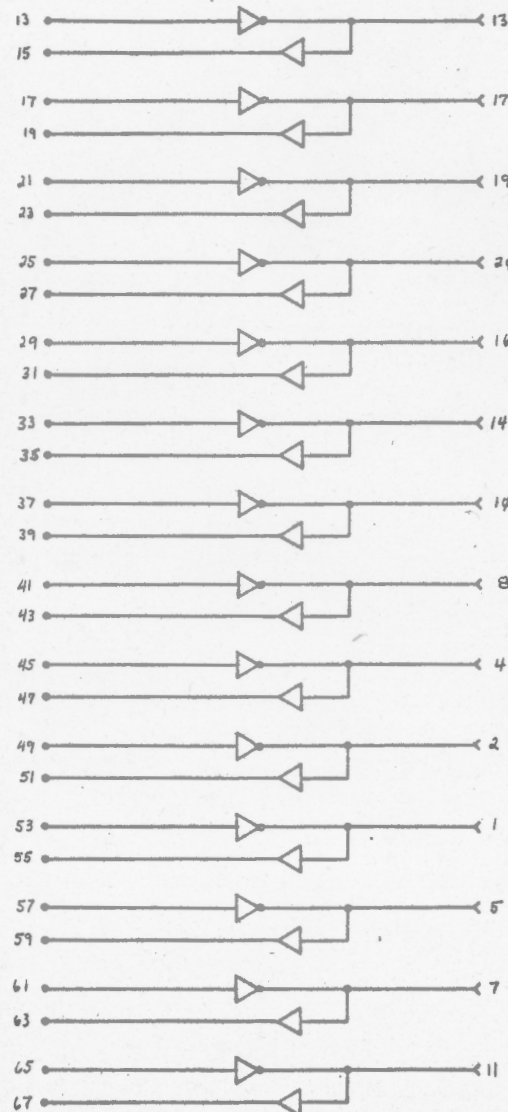
M. Wingfield BH2017-A-9

UCLA LDR

SDS LDR

Twisted Pair

Signal Gnd

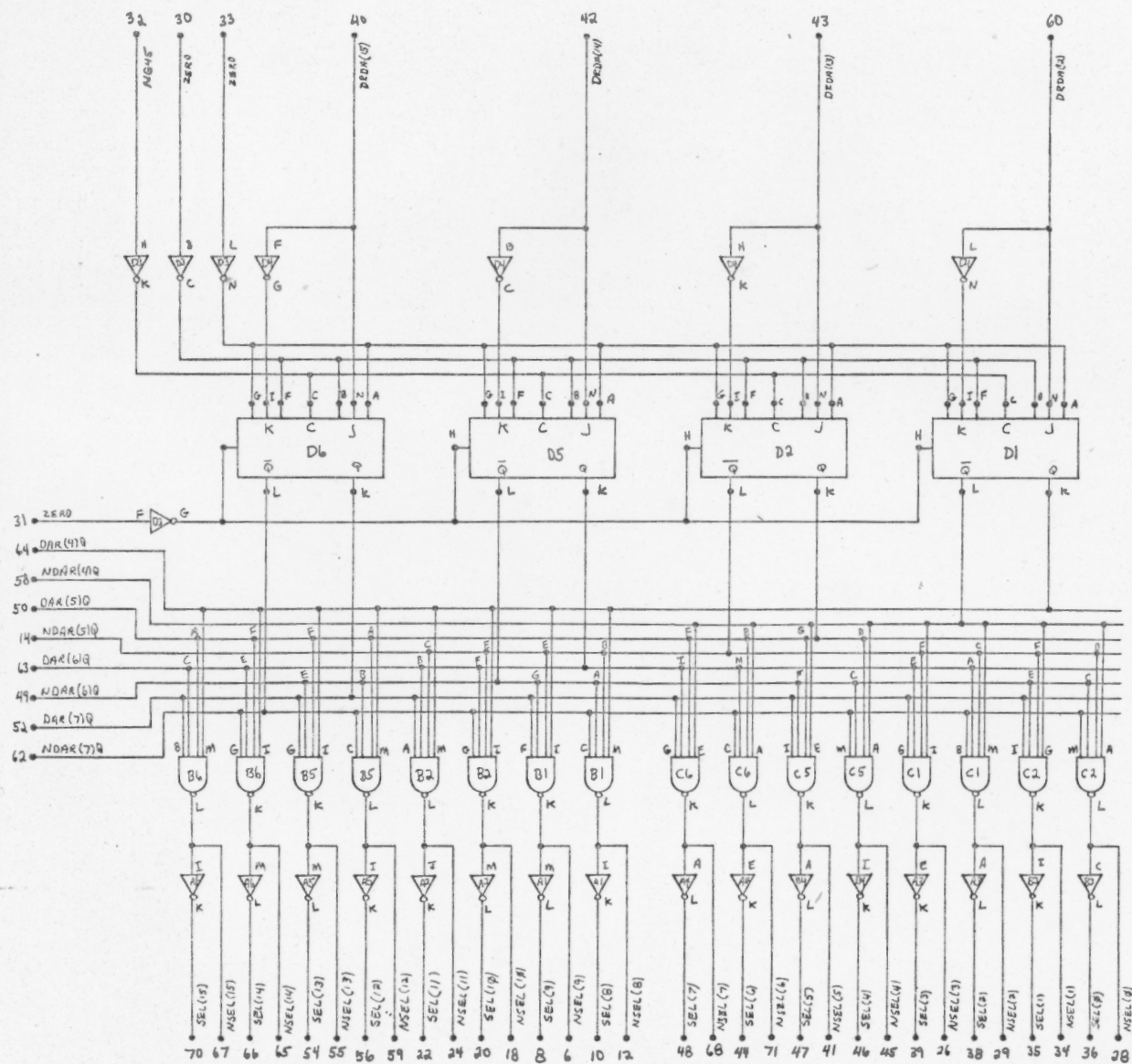


NOTE: Receivers invert for PNP logic inputs

4-Bit Register & Decoder

0003

M. Wingfield BH2817-A-9



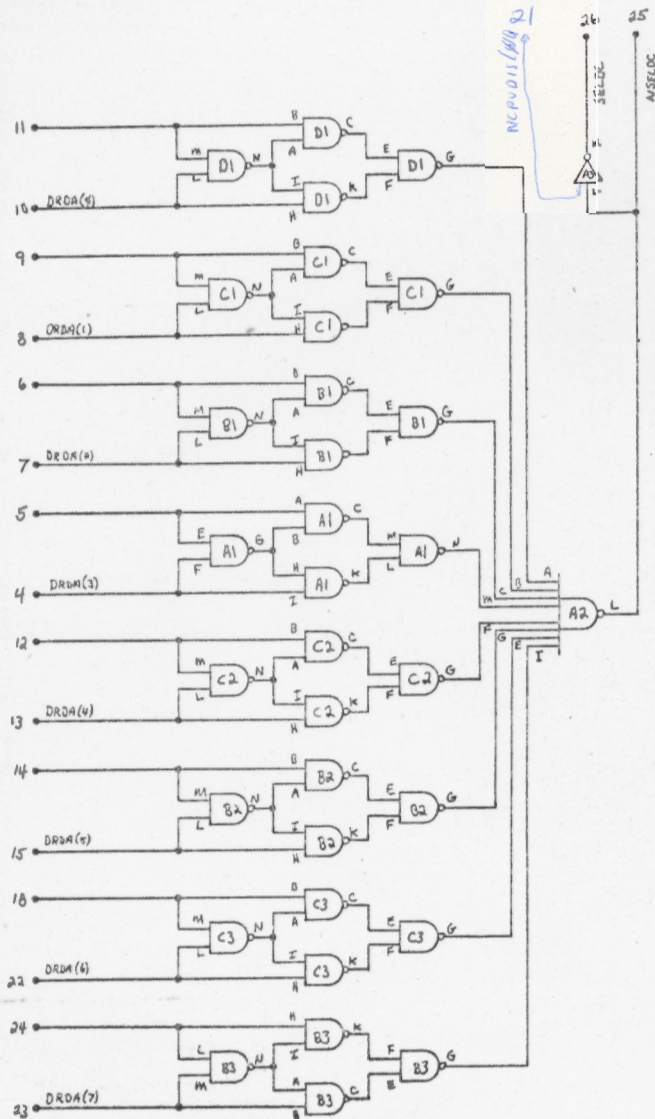
6	5	4	3	2	1	A
56132	56132	56132	56132	56132	56132	A
56132	56132	56132	56132	56132	56132	B
56132	56132	56132	56132	56132	56132	C
56132	56132	56132	56132	56132	56132	D

I.C. Count

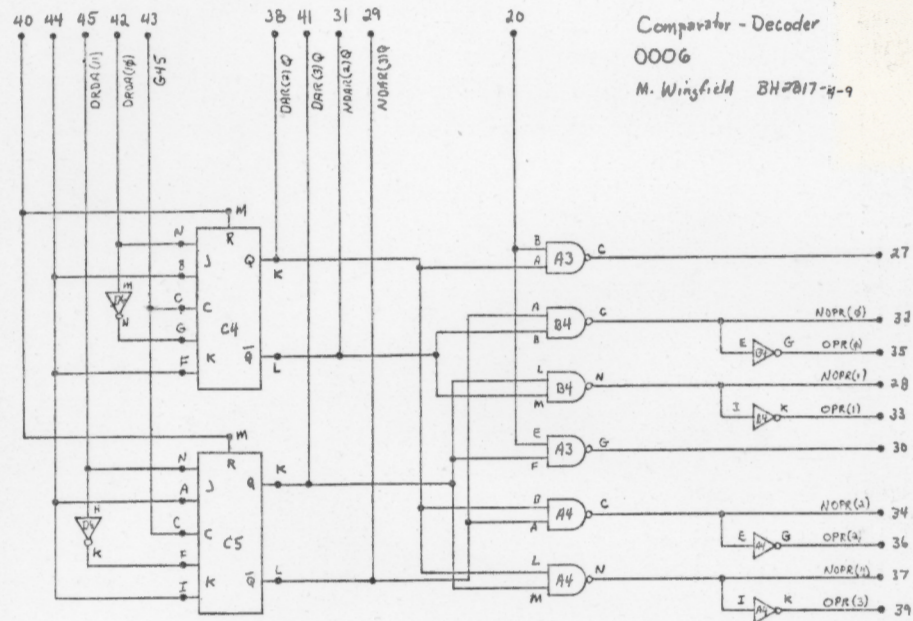
#	Description	Total I.C.
56-22		4
56-132		16
56-142		2
		<u>22 I.C.</u>

I/O: 48

AE619



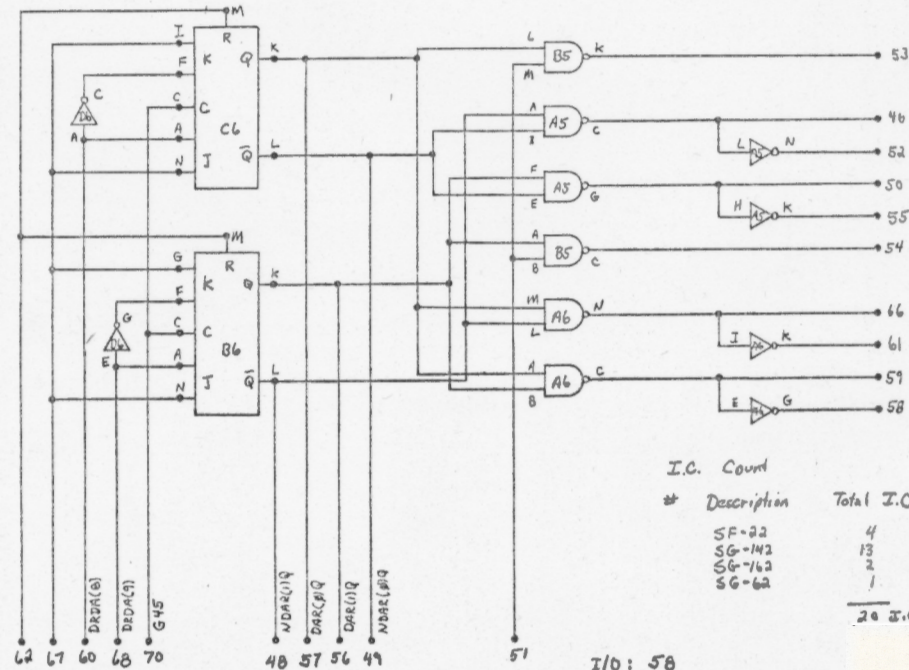
6	5	4	3	2	1
SG142	SG142	SG142	SG142	SG142	SG142
SF22	SG142	SG142	SG142	SG142	SG142
SF22	SF22	SF22	SG142	SG142	SG142
SG142	SF22	SF22	SG142	SG142	SG142



Comparator - Decoder

0006

M. Wingfield BH2017-4-9



I.C. Count

#	Description	Total I.C.
4	SF-22	4
13	SG-142	13
2	SG-742	2
1	SG-62	1

20 I.C.

I/O: 58

ME820